Remarks

In the Office Action mailed July 8, 2005, the Examiner subjected claims 1-181 of the application to a restriction requirement. On page 2 of the aforementioned Office Action, the Examiner indicated that claims 1-17 and 135-181 are drawn to Apparatus of a semiconductor device; while claims 118-134 are drawn to Method of Making a semiconductor device. Pursuant to a telephone conversation with the Examiner, the Office Action contained a typographical error and the Examiner intended to group claims 18-134 and not 118-134.

Applicant hereby elects to proceed with the invention as defined in claims 1-17 and 135-181 in the instant application.

In view of the foregoing, Applicant respectfully asserts that the present application is in condition for examination. Should the Examiner have any questions with regard to the instant response, the Examiner is respectfully requested to contact the undersigned attorney.

Respectfully submitted,

Date: August 8, 2005

Kirk J. DeNiro Reg. No. 35,854

VIERRA MAGEN MARCUS HARMON & DENIRO LLP 685 Market Street, Suite 540 San Francisco, California 94105-4206

Telephone: (415) 369-9660 Facsimile: (415) 369-9665